

FCRC '07

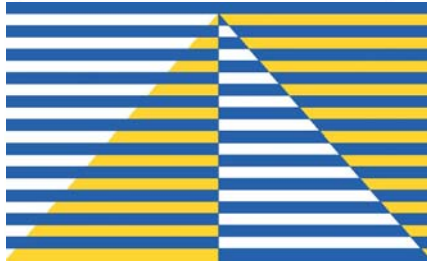
ISCA-34

THE 34th ANNUAL INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE

June 9 – 13, 2007

San Diego, CA

Sponsored by  ACM SIGARCH and  IEEE CS TC



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The *34th International Symposium Computer Architecture* is the premier forum for discussing new ideas and quantified experiences in computer architecture. This year's conference will be held in sunny San Diego, as part of the 2007 Federated Computing Research Conference. Novel papers are solicited on a broad range of topics, including, but not limited to:

- Processor architectures
- Multiprocessor and chip multiprocessor architectures
- Memory system performance
- Storage subsystems
- Interconnection Networks
- Hardware support for parallel programming
- Hardware support for debugging and program correctness
- Hardware support for reliability and fault tolerance
- Hardware support for security
- Hardware support for virtual machines
- Instruction level, thread level, and data level parallelism
- Power and energy efficient architectures
- Application specific, reconfigurable, and embedded architectures
- Network processor and router architectures
- Architectures for emerging technologies and applications
- Impact of technology on architecture
- Architecture modeling and simulation methodology
- Performance evaluation and measurement of real systems

IMPORTANT DATES:

Abstract Deadline: Nov 10th, 2006

Final Paper Deadline: Nov 17th, 2006

No further extensions will be given. Please visit the conference link below for further details on submitting papers:

More detailed submission instructions can be found at the ISCA 34 web site.

ISCA: <http://www.cs.ucsd.edu/isca2007>

FCRC: <http://www.acm.org/fcrc>

